CLAIMS

Having thus described our invention, what we claim as new and desire to secure by Letters Patent is as follows:

1. A method for creating integrated security within electronic devices, comprising the steps of:

concatenating one or more scan chains to create a storage element;

connecting the storage element to a comparator within an electronic circuit wherein an output of the comparator enables a system component;

receiving a password from a user which becomes the system security id code; and

configuring one or more said scan chains to customize the storage element which represents said security id code by blowing integrated electronic fuses.

2. A method according to claim 1, wherein the scan chains are composed of latches or registers and are accessible externally via one or more serial inputs or outputs.

3. A method according to claim 1, wherein the scan chains are sufficiently long in order to represent passwords of variable lengths and to contain a security id code of large magnitude.

4. A method according to claim 1, wherein the security id code is not alterable once blown and cannot be read from the storage elements after the security code is blown except by the comparator.

5. A method according to claim 1, wherein the electronic fuses are blown if the current security code id is provided to enable the securing process to

occur.

6. A method according to claim 1, wherein the password is compared by the comparator to contents of the storage element.

7. A method according to claim 1, wherein the password is validated for size limits and character content.

8. A method according to claim 1, wherein the storage element is a plurality of storage elements.

9. A method according to claim 1, wherein the comparator is a plurality of comparators.

10. A method for creating integrated security within electronic devices, comprising the steps of:

concatenating one or more scan chains to create a storage element said storage element configured by integrated electronic fuses to represent a system security id code;

connecting the memory element to a comparator within an electronic circuit wherein the output of the comparator enables a system component;

receiving a password from a user;

providing the password to the comparator;

comparing the password to the system security id code wherein the comparator output enables a system component.

11. A method according to claim 10, wherein the scan chains are composed of latches or registers and is accessible externally via one or more serial inputs or outputs.

12. A method according to claim 10, wherein the scan chains are sufficiently long in order to represent passwords of variable lengths and to contain a security id code of large magnitude.

13. A method according to claim 10, wherein the security id code is not alterable and cannot be read from the storage elements except by the comparator.

14. A method according to claim 10, wherein the password is compared by the comparator to the contents of the storage element.

15. A method according to claim 10, wherein the password is validated for size limits and character content.

16. A method according to claim 10, wherein the storage elements are a plurality of storage elements.

17. A method according to claim 10, wherein the comparator is a plurality of comparators.

18. An integrated security device for providing security within electronic devices comprising:

a scan chain which is configured using electronic fuses to represent a system security id code;

a comparator that compares a password entered by a system user to the system security id code;

an output of the comparator which can enable a electronic component or electronic device.

19. An integrated security device as recited in claim 18 wherein the scan chain is a plurality of scan chains.

20. An integrated security device as recited in claim 18 wherein the comparator is a plurality of comparators.